

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"20030128720".pn.	US-PGPUB ; USPAT	OR	OFF	2007/05/11 12:13
L5	626	(master SAME clock SAME slave SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)))	US-PGPUB ; USPAT	OR	ON	2007/05/11 12:20
L6	4	(master SAME clock SAME slave SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME tolerance)	US-PGPUB ; USPAT	OR	ON	2007/05/11 13:10
L7	1	(master SAME clock SAME slave SAME ((multiple ADJ tap) OR (multiple-tap)) SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)))	US-PGPUB ; USPAT	OR	ON	2007/05/11 12:18
L8	14	((multiple ADJ tap) OR (multiple-tap)) SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)))	US-PGPUB ; USPAT	OR	ON	2007/05/11 12:19
L9	1	((multiple ADJ tap) OR (multiple-tap)) SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/05/11 12:20
L10	153	(master SAME clock SAME slave SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop))) AND tolerance	US-PGPUB ; USPAT	OR	ON	2007/05/11 12:32
L12	148	(master SAME clock SAME slave SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop))) AND tolerance AND (length OR long\$3 OR short\$3)	US-PGPUB ; USPAT	OR	ON	2007/05/11 12:32
L13	51	(master SAME clock SAME slaves! SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) AND tolerance)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:23
L15	0	(master SAME clock SAME slaves! SAME ((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME MODEM)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:24

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L17	262	"370".clas. AND (((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME MODEM)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:27
L18	34	"370".clas. AND (((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME MODEM SAME CDMA)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:32
L19	20	"375".clas. AND (((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME MODEM SAME CDMA)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:33
L20	53	(((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME MODEM SAME CDMA)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:39
L21	169	(((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME MODEM) AND CDMA	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:52
L22	3090	(((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME driver)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:52
L23	1	(((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME driver SAME boost SAME signal SAME level)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:52
L24	15	(((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) SAME driver SAME boost)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:54
L25	32	(((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) WITH driver WITH (increas\$3 OR boost\$3))	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:56
L26	16	(((phase-locked ADJ loop) OR PLL OR (phase ADJ locked ADJ loop)) WITH driver WITH (increas\$3 OR boost\$3) WITH signal)	US-PGPUB ; USPAT	OR	ON	2007/05/11 15:56
S1	1	"20030128720".pn.	US-PGPUB ; USPAT	OR	OFF	2007/05/11 12:13
S2	38	370/335.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH (master OR reference) WITH clock)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:23

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S3	2	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S4	3	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S5	13	(((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S6	40	370/342.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH (master OR reference) WITH clock)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S7	24	370/335.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S8	22	370/342.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S9	22	370/503.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S10	13	370/516.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:29
S11	3	370/517.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S12	2	370/518.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24
S13	14	375/354.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:24

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S14	19	375/356.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:25
S15	6	375/362.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:29
S16	7	375/371.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30
S17	6	375/373.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:29
S18	5	375/375.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:29
S19	36	375/376.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30
S20	162	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30
S21	148	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30
S22	8	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL)) SAME (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase))	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30
S23	6	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL)) SAME (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase))	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30
S24	41	"370".clas. AND (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30

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S25	68	"375".clas. AND (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:30
S26	6	("4355387" "4808884" "5272694" "5631757" "5751775").PN. OR ("6278868").UREPN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/05 10:07
S27	36	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:31
S28	24	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:31
S29	5	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable WITH (length OR long\$3))	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:31
S30	7	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable WITH (length OR long\$3))	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:31
S31	10	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:31
S32	45	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:32
S33	1	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:32
S34	1	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:32
S35	2	((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:32
S36	5	("4692715" "5748569" "5787265").PN. OR ("6297702").UREPN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/05 11:30

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S37	154	("20010055353" "20020018458" "20020080889" "20020131370" "20030054764" "3668639" "3678204" "3728632" "3875524" "4201892" "4232339" "4506267" "4574378" "4586177" "4587494" "4641317" "4644534" "4651152" "4672608" "4743906" "4763325" "4813057" "4887266" "4979186" "5134408" "5146616" "5148174" "5153595" "5159343" "5253202" "5274271" "5307079" "5307081" "5319218" "5334975" "5355374" "5363108" "5365240" "5473668" "5493691" "5519400" "5523758" "5537414" "5546022" "5586145" "5592177" "5594738" "5610907" "5627829" "5644576" "5675388" "5677927" "5687169" "5687200" "5715236" "5742592" "5748891" "5761197" "5790551" "5812671" "5815537" "5832035" "5850422" "5886652" "5889767" "5901172" "5903605" "5909469" "5909491" "5920278" "5926501" "5940435" "5940452" "5952956" "5953344" "5960000" "5970062" "5977822" "6002687" "6002708" "6014374" "6026125" "6031862" "6041051" "6055411" "6064697" "6067648" "6069887" "6091717" "6094464" "6097707" "6097766" "6115390" "6122291" "6133875" "6133876" "6161138" "6172965" "6177903" "6178217" "6181687" "6212230" "6226277" "6232910" "6236662" "6239741" "6243372" "6243583" "6246277" "6246377" "6246702" "6275500" "6275544" "6278713" "6285662" "6285873" "6292153" "6300903" "6310865" "6324397" "6347084" "6351468" "6351652") .PN. OR "6385461" "6400308" "6424645" "6437832" "6449265" "6463042"	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/05 13:49
5/11/07 4:29:04 PM	C:\Documents and Settings\Eng\My Documents\	EAST\Workspaces\10044625.wsp			Page 6	

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S38	12	("4004100" "4247936" "4532547" "4876719" "4885638" "5566174" "5710774").PN. OR ("6339601").URPN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/05 14:00
S39	23	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH master WITH slave WITH synchroniz\$)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:32
S40	13	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH master WITH slave WITH synchroniz\$)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:32
S41	61	("4201892" "4586177" "4587494" "4672608" "4887266" "4979186" "5146616" "5159343" "5253202" "5274271" "5307079" "5319218" "5355374" "5363108" "5473668" "5537414" "5546022" "5594738" "5627829" "5644576" "5677927" "5687169" "5687200" "5742592" "5748891" "5790551" "5815537" "5832035" "5901172" "5909469" "5920278" "5926501" "5940452" "5952956" "5953344" "5960000" "5970062" "5977822" "6002708" "6014374" "6026125" "6031862" "6041051" "6055411" "6091717" "6094464" "6097707" "6133875" "6133876" "6226277" "6243583" "6246377" "6246702" "6278713" "6285662" "6285873" "6300903" "6347084" "6351652" "6424645" "6633558").PN. OR ("6944148").URPN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/05 14:34
S42	33	("3562432" "3646444" "3654395" "4811340" "5184350" "5541929"). PN. OR ("5666366").URPN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/05 14:42
S43	14	("5546052").PN. OR ("5734301").URPN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/06 10:42

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S44	105	("20010055353" "20020018458" "20020131370" "20030054764" "3668639" "3678204" "3728632" "3875524" "4232339" "4506267" "4574378" "4641317" "4644534" "4651152" "4672608" "4743906" "4763325" "4813057" "5134408" "5146616" "5148174" "5153595" "5159343" "5307081" "5334975" "5365240" "5493691" "5519400" "5523758" "5586145" "5592177" "5594738" "5610907" "5644576" "5675388" "5677927" "5687169" "5715236" "5748891" "5761197" "5812671" "5832035" "5850422" "5886652" "5889767" "5903605" "5909491" "5920278" "5940435" "6002708" "6026125" "6031862" "6041051" "6064697" "6067648" "6069887" "6097766" "6122291" "6133875" "6133876" "6161138" "6172965" "6177903" "6178217" "6181687" "6212230" "6232910" "6236662" "6239741" "6246277" "6275500" "6292153" "6300903" "6310865" "6324397" "6351468" "6351652" "6385461" "6400308" "6424645" "6437832" "6449265" "6463042" "6477171" "6480505" "6492904" "6497656" "6505032" "6510150" "6519460" "6539213" "6549567" "6574266" "6597683" "6603818" "6621857" "6625229" "6628642" "6633558" "6668008" "6697382" "6707828" "6735238" "6735734" "6747959"). PN. OR ("6952456").URPN.	US-PGPUB ; USPAT; USOCR	OR	OFF	2006/04/06 10:51
S45	31	"370".clas. AND (phase WITH tolerance WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:32
S46	38	"375".clas. AND (phase WITH tolerance WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:33

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S49	8	JONES-DELON-K.in.	US-PGPUB ; USPAT	OR	OFF	2007/05/10 15:03
S54	41	370/335.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH (master OR reference) WITH clock)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:33
S55	2	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:04
S56	3	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:05
S57	42	370/342.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH (master OR reference) WITH clock)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:33
S58	16	(((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:05
S59	26	370/335.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:34
S60	23	370/342.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:34
S61	24	370/503.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:34
S62	3	370/517.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:34
S63	2	370/518.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:34

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S64	15	375/354.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:35
S65	20	375/356.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:35
S66	7	375/362.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:35
S67	6	375/373.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:35
S68	18	370/516.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:35
S69	6	375/375.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:36
S70	7	375/371.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:36
S71	37	375/376.ccls. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/03/30 16:36
S72	176	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:06
S73	160	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:06
S74	9	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL)) SAME (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:07

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S75	8	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL)) SAME (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:06
S76	79	"375".clas. AND (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:07
S77	47	"370".clas. AND (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:07
S78	39	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:07
S79	25	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:07
S80	5	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable WITH (length OR long\$3))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:07
S81	7	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable WITH (length OR long\$3))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:07
S82	12	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:05
S83	58	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:05
S84	1	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:08
S85	1	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:08
S86	2	((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:08

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S87	25	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH master WITH slave WITH synchroniz\$)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:08
S88	14	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH master WITH slave WITH synchroniz\$)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:06
S89	37	"370".clas. AND (phase WITH tolerance WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:08
S90	43	"375".clas. AND (phase WITH tolerance WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:09
S91	1	(slave SAME sector SAME PLL SAME tolerance SAME longest).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:09
S92	2	(master SAME clock SAME signal SAME delay SAME tolerance).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:09
S93	1	(master SAME slave SAME longest SAME cable).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:09
S95	1	(tolerance SAME longest SAME cable).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:09
S96	1	(slave SAME predefined SAME tolerance SAME matching).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:09
S97	2	10/017591	US-PGPUB ; USPAT	OR	OFF	2007/04/02 09:42
S98	9	JONES-DELON-K.in.	US-PGPUB ; USPAT	OR	OFF	2007/05/10 15:40
S99	2	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:10
S10 0	3	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:10
S10 1	16	(((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME (master OR reference) SAME clock SAME longest)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:10
S10 2	12	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:10

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S10 3	58	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:10
S10 4	15	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH master WITH slave WITH synchroniz\$)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:10
S10 5	180	"375".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:10
S10 6	162	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) SAME master SAME clock SAME slave)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:11
S10 7	8	"370".clas. AND ((((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL)) SAME (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:11
S10 8	9	"375".clas. AND ((((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL)) SAME (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:11
S10 9	79	"375".clas. AND (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:11
S11 0	47	"370".clas. AND (slave WITH (select\$3 OR choos\$3 OR chos\$3) WITH phase)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 1	39	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 2	25	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 3	5	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable WITH (length OR long\$3))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12

EAST Search History

S11 4	7	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH cable WITH (length OR long\$3))	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 5	1	"370".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 6	1	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 7	1	"375".clas. AND ((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 8	26	"370".clas. AND (((phase-lock\$2 ADJ loop) OR (phase ADJ lock\$2 ADJ loop) OR PLL) WITH master WITH slave WITH synchroniz\$)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S11 9	2	((select\$3 OR choos\$3 OR chos\$3) WITH phase WITH match\$3 WITH delay WITH cable)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S12 0	38	"370".clas. AND (phase WITH tolerance WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:12
S12 2	43	"375".clas. AND (phase WITH tolerance WITH delay)	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:13
S12 3	1	(slave SAME sector SAME PLL SAME tolerance SAME longest).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:13
S12 4	2	(master SAME clock SAME signal SAME delay SAME tolerance).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:13
S12 5	1	(master SAME slave SAME longest SAME cable).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:13
S12 6	1	(tolerance SAME longest SAME cable).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:13
S12 7	1	(slave SAME predefined SAME tolerance SAME matching).CLM.	US-PGPUB ; USPAT	OR	ON	2007/05/10 15:13
S12 8	1	"20030128720".pn.	US-PGPUB ; USPAT	OR	OFF	2007/05/10 15:40